

[Web Images](#) [Videos](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more](#) ▼

[Sign in](#)

Google scholar

PSoC Designer

Search

[Advanced Scholar Search](#)

[Scholar Preferences](#)

Scholar [All articles](#) [Recent articles](#)

Results 1 - 10 of about 1,840. (0.11 sec)

Just add sensor-integrating analog and digital signal conditioning in a programmable ...

D Seguire - Sensors, 2002. Proceedings of IEEE, 2002 - [ieeexplore.ieee.org](#)

... These are built into the development tool, the **PSoC Designer** Device Editor as User Modules. The Switched Capacitor Blocks are more complex. ...

[Cited by 14](#) - [Related articles](#)

[PDF] •Motor Tachometer Speed Calculation Using Hardware Timer Capture Feature

A Motley, AP Family, AA Notes - 2002 - [application-notes.digchip.com](#)

... Motley Associated Project: Yes Associated Part Family: CY8C24x23A, CY8C27x43, CY8C29x66

GET FREE SAMPLES HERE Software Version: **PSoC Designer**™ 4.4 Associated ...

[Cited by 2](#) - [View as HTML](#)

An architecture for a configurable mixed-signal device- •ed.ac.uk [PDF]

M Mar, B Sullam, E Blom, CMS Inc, WA ... - IEEE Journal of Solid-State Circuits, 2003 - [ieeexplore.ieee.org](#)

... 4. To facilitate programming of the **PSOC** architecture, a soft- ware tool (the **PSoC Designer**) uses preconfigured "user mod- ules." A user module is defined ...

[Cited by 12](#) - [Related articles](#) - [BL Direct](#) - [All 3 versions](#)

Debug methods for hybrid CPU/FPGA systems- •byu.edu [PDF]

E Roesler, B Nelson - 2002 IEEE International Conference on Field- ..., 2002 - [ieeexplore.ieee.org](#)

... Having hardware at the beginning of development allows the **designer** to begin testing parts of **PSOC** designs in hardware earlier, before the whole sys- tem is ...

[Cited by 11](#) - [Related articles](#) - [All 7 versions](#)

[CITATION] PSoC Express

PSC **Designer** - PSoC Programmer

[Cited by 1](#) - [Related articles](#)

[CITATION] V4, 2: Integrated Development Enviroment User Guide

P **Designer**

[Cited by 1](#) - [Related articles](#)

[CITATION] C Language Compiler User Guide

PSC **Designer** - CYPRESS 社, 2005

[Cited by 1](#) - [Related articles](#)

[CITATION] Cypress Semiconductor

PSC **Designer**

[Cited by 1](#) - [Related articles](#)

[book] Designer's guide to the Cypress PSoC

R Ashby - 2005 - books.google.com

... 26 Supervisory ROM 29 Interrupt Controller 30 General-Purpose I/O 31 Analog I/O
40 Digital and Analog Programmable Blocks 43 Chapter 3: **PSoC Designer** 53 Device ...

[Cited by 7](#) - [Related articles](#) - [All 11 versions](#)

Deploying Simulink Models into System-On-Chip Structures

G Chindris, M Muresan - Electronics Technology, 2006. ISSE'06. 29th ..., 2006 - ieeexplore.ieee.org

... that fully integrates with **PSoC Designer**, providing comprehensive debugging
of device configuration and the user's application. ...

[Cited by 2](#) - [Related articles](#)

Goooooooooooooogle ►
Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

PSoC Designer

Search

[Go to Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2009 Google